EFM Synthesizers ADSR - 3508

## 2408 ADSR Envelope Generator

# **Description**

Q1 and Q2 form a non-inverting level shifter.

Start: (Gate On)

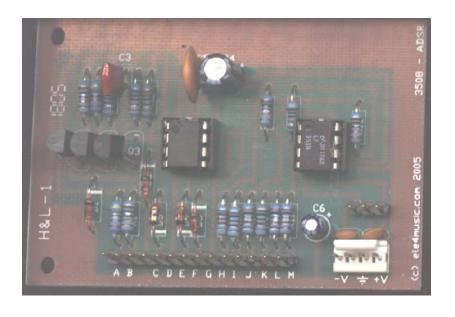
When there is a gate on the base of Q1 the collector is low and Q2 turns off. R4 pulls the collector high and C3 quickly charges and discharges through R5 and D2 causing Q3 to send a low going pulse on the trigger input (pin-2) of the 555. R4 also pulls the 555s reset pin high and removes the discharge path for release diode D5.

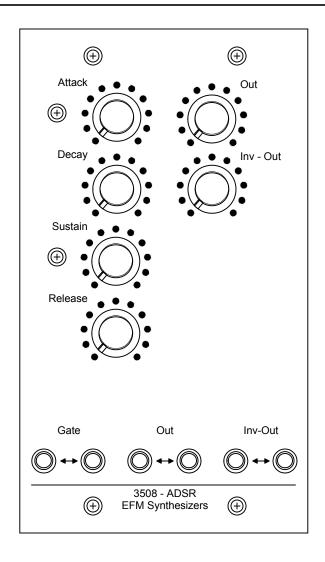
### Attack:

When the 555 is triggered the output (pin-3) goes high and starts to charge C5 through the attack diode D4 and P3. P3 sets the charge rate.

# Decay:

When the 555s threshold (pin-6) is reached the 555s output goes low. R4 is still holding D5 high so C5 starts to discharge through R9, D3, P2 and P1 through the 555s discharge transistor. P2 sets the decay rate.





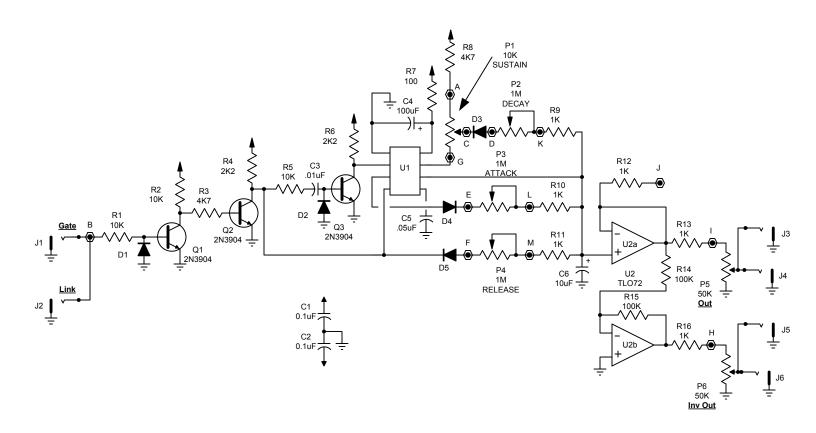
#### Sustain:

C5 will continue to discharge until it reaches the voltage level set by the voltage divider formed by the 555s internal discharge transistor and P1. P1 sets the sustain level

Release: (Gate Off)

When the gate is removed the collector of Q2 goes low. If this happens before the threshold is reached. the 555s reset (pin-4) is pulled low and the 555 is reset. C5 starts to discharge through R11, P4, D5 and Q2 to ground. P4 sets the discharge rate.

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Small Kit		
PCB	PC Board	1
C1-3	0.1uF Ceramic	3
C4	100uF Ele	1
C5	.05uF Ele	1
C6	10uF Ceramic	1
R1,2,5	10K	3
R3,8	4.7K	2
R4,6	2K2	2
R7,14,15	100K	3
R9-13,16	1K	6
D1-5	1N4148	5
Q1-3	2N3904	3
U1	LM555	1
U2	TLO72	1

Full Kit		
P1	10K Pot	1
P2-4	1M Pot	3
P5,6	50K Pot	2
Knob		6
Jack	1/8"	6
L Bracket w/hardware		2
Header		1
Panel		1
Overlay		1

